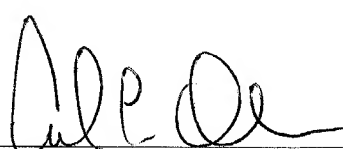



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TRANSMITTAL OF FORMAL DRAWINGS				Docket No. BUR920000228US1	
In Re Application Of: Molinowski et al.					
Serial No. 09/683,027	Filing Date 11/09/2001	Batch No. MAR 05 2002	Examiner	Art Unit 2835	
Invention: DUAL CHIP STACK METHOD FOR ELECTRO-STATIC DISCHARGE PROTECTION OF INTEGRATED CIRCUITS					
Address to: Assistant Commissioner for Patents Washington, D.C. 20231					
Transmitted herewith are: 9 sheets of formal drawing(s) for this application. <input checked="" type="checkbox"/> Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).					
 Signature			Dated: 2-27-2002		
Arlen L. Olsen Reg. No. 37,543 Schmeiser, Olsen & Watts 3 Lear Jet Lane, Suite 201 Latham, NY 12110 (518) 220-1850			<div>RECEIVED MAR 11 2002 TC 2800 MAIL ROOM</div> <div>I certify that this document and attached formal drawings are being deposited on 2-27-02 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.</div> <div> Signature of Person Mailing Correspondence</div> <div>Dorothea Rubbone Typed or Printed Name of Person Mailing Correspondence</div>		